

<b>Pin Name (1)</b>	<b>44-Pin PLCC</b>	<b>44-Pin PQFP/TQFP</b>
INPUT/GCLK1	43	37
INPUT/GCLRn	1	39
INPUT/OE1	44	38
INPUT/OE2/GCLK2 (2)	2	40
TDI (3)	7	1
TMS (3)	13	7
TCK (3)	32	26
TDO (3)	38	32
PDn (4)	3	41
GND	10, 22, 30, 42	4, 16, 24, 36
VCC	3, 15, 23, 35	9, 17, 29, 41
No Connect (N.C.)	–	–
Total User I/O Pins (5)	36	36

<b>LAB</b>	<b>MC</b>	<b>44-Pin PLCC</b>	<b>44-Pin PQFP/TQFP (1)</b>
A	1	4	42
A	2	5	43
A	3	6	44
A	4	7 (3)	1 (3)
A	5	8	2
A	6	9	3
A	7	11	5
A	8	12	6
A	9	13 (3)	7 (3)
A	10	14	8
A	11	16	10
A	12	17	11
A	13	18	12
A	14	19	13
A	15	20	14
A	16	21	15
B	17	41	35
B	18	40	34
B	19	39	33
B	20	38 (3)	32 (3)
B	21	37	31
B	22	36	30
B	23	34	28
B	24	33	27
B	25	32 (3)	26 (3)
B	26	31	25
B	27	29	23
B	28	28	22
B	29	27	21
B	30	26	20
B	31	25	19
B	32	24	18

**Notes:**

- (1) EPM7032S and EPM7032V devices are not available in the 44-pin PQFP package.
- (2) The GCLK2 function is available in MAX 7000S and MAX 7000E devices only.
- (3) This JTAG pin applies to MAX 7000S devices only and this pin may function as either a JTAG port or a user I/O pin. If the device is configured to use the JTAG ports for ISP, this pin is not available as a user I/O pin.
- (4) The PDn pin is available in EPM7032V devices only.
- (5) The user I/O pin count includes dedicated input pins and all I/O pins.

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